

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	12844 4	map\$4 with(space or area or location or region or range or block)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:04
2	BRS	L2	30468	1 with(memory or storage or ram or rom or nvram or nvrom)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:04
3	BRS	L3	26782 2	(processor or cpu or microprocessor or microcontroller)with(space or area or location or region or range or block)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:04
4	BRS	L4	40601	3 with(inside or internal or within)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:05
5	BRS	L5	1221	2 same 4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:05
6	BRS	L6	4537	trap\$4 with(code or opcode or kernel or instruction)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:05

	Type	L #	Hits	Search Text	DBs	Time Stamp
7	BRS	L7	0	5 same 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:05
8	BRS	L8	59	5 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:06
9	BRS	L9	714	trap\$4 adj4 handler	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:06
10	BRS	L10	0	5 same 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:06
11	BRS	L11	19	5 and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:06
12	BRS	L12	19	11 and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:06

	Type	L #	Hits	Search Text	DBs	Time Stamp
13	BRS	L13	824	harvard adj4 architecture	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:07
14	BRS	L14	58	5 and 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	2005/05/21 13:07